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CENTRAL FAX CENTER

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE FEB 07 2005

Patent Application Serial No. 09/234,233
Filing Date January 20, 1999
Inventor Weimin Li
Assignee Micron Technology, Inc.
Group Art Unit 2818
Examiner D. Vu
Attorney Docket No. MI22-1035
Title: Semiconductor Processing Methods

Commissioner for Patents
PO Box 1450
Art Unit 2818
Alexandria, VA 22313-1450

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2. Transmittal form PTO/SB/21
3. Information Disclosure Statement; PTO-1449, cited reference

Dated: Feb. 7, 2005By: 

Gail Wright

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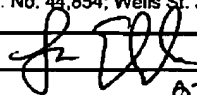
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/234,233
	Filing Date	January 20, 1999
	First Named Inventor	Weimin Li
	Art Unit	2818
	Examiner Name	D. Vu
Total Number of Pages in This Submission	Attorney Docket Number	M122-1035

ENCLOSURES (Check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	James E. Lake Reg. No. 44,854; Wells St. John P.S.
Signature	
Date	07 Feb 2005

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Attorney's Docket No. MI22-1035
Title: Semiconductor Processing Methods

INFORMATION DISCLOSURE STATEMENT


References – See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patent listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed after the filing of a Request for Continued Examination (RCE) Application and prior to the receipt of the first Office Action. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Citation of this reference is respectfully requested.

Respectfully submitted,

Dated: 07 Feb 2005By: 
James E. Lake
Reg. No. 44,854

Sheet 1 of 1

200 Form PTO-1449 LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1035		SERIAL NO. 09/234,233	
APPLICANT Weimin Li				FILING DATE January 20, 1999		GROUP 2818	

U.S. PATENT DOCUMENTS							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	6,121,133	09/2000	Iyer et al			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS							
	Document Number	Date	Country	Class	Subclass	Translation	
	AL					Yes	No
	AM						
	AN						

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)			
	AO		Wolf et al, Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, pp. 1 and 2.
	AP		
	AO		

EXAMINER	DATE CONSIDERED
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.